# The CWC Authenticated Encryption (Associated Data) Mode 

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#### Abstract

We introduce CWC, a new block cipher mode of operation designed to protect both the privacy and the authenticity of encapsulated data. Important properties of CWC include: 1. Performance. CWC is parallelizable and is efficient in both hardware and software. 2. Security. CWC is provably secure and its provable security depends only on the pseudorandomness of the underlying block cipher. No other cryptographic primitives are used and no other assumptions are made. 3. Patent-free. To the best of our knowledge CWC is not covered by any patents.

CWC is currently the only dedicated authenticated encryption with associated data (AEAD) scheme that simultaneously has these three properties (e.g., CCM and EAX are not parallelizable and OCB is not patent-free). Having all three of these properties makes CWC a strong candidate for use with future high-performance systems.


## 1 Introduction

There has recently been significant interest in developing block cipher modes of operation capable of simultaneously protecting both the privacy and the authenticity/integrity of encapsulated data. Such modes of operation are often called authenticated encryption ( $A E$ ) schemes or, if the schemes are capable of authenticating more data than they encrypt, authenticated encryption with associated data (AEAD) schemes.

In this work we propose a dedicated AEAD scheme that is patent-free, provably-secure, parallelizable and efficient in both hardware and software. To our knowledge, ours is the only dedicated AEAD scheme that simultaneously has all of these properties. (Our construction also has other desirable properties. For example, it is clean and simple (in our opinion), on-line, uses a single block cipher key, and allows for pre-processing of associated data or other header fields.)

Our general construction, CWC, is based on what is called "the Encrypt-then-Authenticate generic composition paradigm." In particular, CWC essentially combines a Carter-Wegman message authentication scheme [14] with CTR mode encryption in an Encrypt-then-Authenticate manner. The general idea is as follows: given a pair of strings $(A, M)$ and a nonce $N$ as input, the CWC encapsulation algorithm encrypts $M$ with CTR mode to get some intermediate ciphertext $\sigma$. It then uses a Carter-Wegman MAC and the nonce $N$ to MAC the pair $(A, \sigma)$. If we let $\tau$ denote the resulting MAC tag, then the output of the CWC encapsulation algorithm is the concatenation of $\sigma$ and $\tau$. CWC is designed to protect the privacy of $M$ and the integrity of both $A$ and $M$. Although based on the Encrypt-then-Authenticate generic composition paradigm, CWC is not a generic composition construction; for example, the CWC encryption and MAC components share the same block cipher key. This means, among other things, that we had to prove the security of CWC directly, rather than invoke previous results about the generic composition paradigm.

Let us begin by discussing some of the motivations for CWC.
Why do we want dedicated authenticated encryption schemes? The traditional approach to achieving authenticated encryption is to combine some standard encryption scheme (e.g., CBC mode) with some standard message authentication scheme (e.g., HMAC). This is known as the generic-composition approach and was first explored in [1] and [8]. Unfortunately, such generic-composition constructions are often $a d$ hoc and, as illustrated in [1] and [8], it is very easy to accidentally combine secure encryption schemes with secure MACs and still get insecure authenticated encryption schemes.

One of the biggest advantages of dedicated AEAD schemes over generic-composition AEAD schemes is that dedicated AEAD schemes are not prone to such accidental errors. That is, since dedicated AEAD schemes clearly specify how to achieve both privacy and authenticity, there is no longer the risk of someone accidentally combing a privacy/encryption component with an authenticity/MAC component in an insecure fashion. Furthermore, since most applications that require privacy also require integrity, it is logical to focus on tools capable of providing both services simultaneously. There is thus great value in developing and standardizing dedicated AEAD schemes, as evidenced by a wealth of papers in this area $[2,4,5,6,11,12,15]$.
Patents. Pragmatically, patents are a major impediment to the standardization and wide-spread deployment of some of the modes presented in the above-mentioned papers. In particular, three independent parties have applied for patents on single-pass authenticated encryption schemes. It is not our purpose to describe the specifics of these patent applications (and, indeed, the specifics are not completely known to the public). Rather, we point out that the existence of these patent applications makes many existing authenticated encryption modes less attractive, and therefore less amenable to standardization and deployment. To exemplify this point, we note that although

Rogaway, Bellare, Black, and Krovetz's OCB mode [12] is very efficient and elegant, it was apparently rejected from the IEEE 802.11 wireless working group largely because of the fact that it was covered by patent applications from multiple parties.
What is needed? Noting the need for patent-free dedicated AEAD schemes, Whiting, Ferguson, and Housley proposed a patent-free AEAD scheme called CCM [15] which, apparently because of its patent-free nature, has been adopted by the IEEE 802.11 working group. CCM was recently followed by another construction, called EAX, by Bellare, Rogaway, and Wagner [2]. Since CCM and EAX are based on the generic-composition approach (they both essentially combine standard CTR mode encryption with variants of CBC-MAC message authentication), CCM and EAX do not fall under the aforementioned patent applications.

There is, however, one significant disadvantage with both CCM and EAX: the CCM and EAX encryption and decryption operations are not parallelizable. That is, although the CTR mode portions of CCM and EAX are clearly parallelizable, their CBC-MAC portions are not. Parallelizability is, however, very important. For example, without the ability to parallelize the encryption process, using current technology it does not seem possible to build a single hardware engine for CCM or EAX capable of encrypting beyond approximately 2 Gbps. ${ }^{1}$ Although 2 Gbps might be adequate for today's applications, such speeds will not be adequate for the coming 10 Gbps network devices.

Therefore, there is a need for a patent-free dedicated mode of operation capable of encrypting and authenticating data at 10 Gbps .
The CWC solution. We propose a general paradigm, called CWC, that addresses all the aforementioned issues. In particular, our preferred instantiation of CWC for 128-bit block ciphers is un-patented, provably-secure, parallelizable, and efficient in both hardware and software. The parallelizability enables high-speed CWC hardware implementations to encrypt at 10 Gbps when using AES.

Throughout the body of this paper we will focus on our instantiation of the CWC paradigm for 128 -bit block ciphers. ${ }^{2}$ In particular, we focus on CWC-AES-kl, a CWC instantiation with AES-kl as the underlying block cipher (here AES-kl denotes kl-bit AES, where the key length $\mathrm{kl} \in$ $\{128,192,256\})$. When our results apply to AES with all key lengths, we shall simply refer to CWC-AES. Instead of writing CWC-AES-kl for some appropriate kl, we shall write CWC-BC or simply CWC when we mean the general CWC paradigm instantiated like CWC-AES-kI but with any 128 -bit block cipher BC in place of AES-kl.

Note the difference in font between CWC, the general paradigm, and CWC, our specific proposal.
Achieving parallelism. Clearly the CTR mode portion of CWC is parallelizable. Furthermore, the core of the Carter-Wegman MAC portion of CWC (a.k.a. the universal hashing portion of CWC) can be made parallelizable. In the case of CWC, the universal hashing step works by computing

$$
Y_{1} x^{n}+Y_{2} x^{n-1}+Y_{3} x^{n-2}+Y_{4} x^{n-3}+\cdots+Y_{n} x+Y_{n+1} \bmod 2^{127}-1
$$

where $Y_{1}, \ldots, Y_{n+1}$ are 96 -bit integers ${ }^{3}$ corresponding to the pair $(A, \sigma)$ and $x$ is an integer modulo the prime $2^{127}-1$. It is well-known that the computation of this polynomial is parallelizable. For

[^0]example, if we have two processors available, we can rewrite the above polynomial as
$$
\left(Y_{1} y^{m}+Y_{3} y^{m-1}+\cdots+Y_{n}\right) x+\left(Y_{2} y^{m}+Y_{4} y^{m-1}+\cdots+Y_{n+1}\right) \bmod 2^{127}-1
$$
where $y=x^{2} \bmod 2^{127}-1, m=(n-1) / 2$, and we assume for illustrative purposes that $n$ is odd. We can then compute both the left and the right portions of the above in parallel. Additional parallelism can be achieved by further splitting the original polynomial into $j$ polynomials in $y^{\prime}=$ $x^{j} \bmod 2^{127}-1$.

Single key. The CWC paradigm uses a single block cipher key $K$. The key $K$ is used in all applications of the underlying block cipher and is used to derive a subkey $K_{h}$ for use in CWC's universal hashing step. In the case of our CWC instantiation, deriving $K_{h}$ requires one block cipher invocation. The main advantage with deriving subkey $K_{h}$ from $K$ is that it simplifies key management and reduces the costs associated with fetching key material in hardware, which can be a bottleneck.
Performance. Let $(A, M)$ be some input to the CWC encapsulation algorithm (recall that $A$ is the associated data and $M$ is the message to encrypt). Assuming that the universal hashing subkey is maintained across invocations, encapsulating $(A, M)$ takes $\lceil|M| / 128\rceil+2$ block cipher invocations. The polynomial used in CWC's universal hashing step will have degree $d=\lceil|A| / 96\rceil+\lceil|M| / 96\rceil$. There are several ways to evaluate this polynomial (details in [7]). For example, assuming no precomputation, we could evaluate this polynomial using $d 128$-by- 128 -bit multiplies. As another example, assuming $n$ precomputed powers of the hash subkey, which are cheap to maintain in software for reasonable $n$, we could evaluate the polynomial using $d-m 96$-by-128-bit multiplies and $m$ 128-by-128-bit multiplies, where $m=\lceil(d+1) / n\rceil-1$.

As noted before, it is possible to implement CWC-AES in hardware at 10 Gbps using conventional ASIC technology. Specifically, at 0.13 micron, it should take about 300 Kgates to reach 10 Gbps throughput. In software, experimental results show that the current best implementation of CWC-AES-128 on a Pentium III (due to Brian Gladman) runs significantly faster than CCM and EAX with 128-bit AES and implemented with popular crypto libraries. Using the precomputation approach from Bernstein [3], we anticipate reducing the cost of CWC's universal hashing step to around 8 cpb , thereby significantly improving the performance of CWC-AES in software.
Provable security. CWC is a provably-secure AEAD scheme assuming that the underlying block cipher is a secure pseudorandom function or pseudorandom permutation. Consequently, if we believe AES to be a secure pseudorandom permutation (which is a widely-held belief), then CWC-AES is secure. For our proofs of security, we use Rogaway's AEAD notions from [11]. In our provable security results we clearly show that the same block cipher key can be used in CWC's CTR mode portion, in the generation of the hash subkey $K_{h}$, and in the block cipher applications used within CWC's message authentication portion.

### 1.1 Background and related work

The notion of an authenticated encryption (AE) scheme was formalized in Katz-Yung [6] and Bellare-Namprempre [1] and the notion of an authenticated encryption with associated data (AEAD) scheme was formalized in Rogaway [11]. In [1, 8], Bellare-Namprempre and Krawczyk explored ways to combine standard encryption schemes with MACs to achieve authenticated encryption. A number of dedicated AE and AEAD schemes also exist, including RPC [6], XCBC [4], IACBC [5], OCB [12], CCM [15], and EAX [2]. Within the scope of dedicated block cipher-based AEAD schemes, CWC's closest relatives are CCM and EAX, which also use two passes and are unpatented. From a broader perspective, CWC is similar to the combination of McGrew's UST [10]
and TMMH [9], where one of the main advantages of CWC over UST+TMMH is CWC's small key size, which can be a bottleneck for UST+TMMH in hardware at high speeds.

Rogaway and Wagner recently released a critique of CCM [13]. For each issue raised in [13], we find that we have already addressed the issue (e.g., we designed CWC to be on-line) or we disagree with the issue (e.g., we feel that it is sufficient for new modes of operation to handle arbitrary octet-length, as opposed to arbitrary bit-length, messages ${ }^{4}$ ).

The integrity portion of CWC builds on top of the Carter-Wegman universal hashing approach to message authentication [14]. Like Bernstein's hash127 [3], CWC's universal hash function evaluates a polynomial over the integers modulo the prime $2^{127}-1$. The main difference between hash127 and the CWC universal hash function is that hash127 uses signed 32 -bit coefficients and CWC uses unsigned 96 -bit coefficients.

In April 2003 we introduced an Internet-Draft, within the IRTF Crypto Forum Research Group, specifying the CWC-AES mode of operation. The latest version of the Internet-Draft can be found at http://www.zork.org/cwc or on the IETF website http://www.ietf.org.

### 1.2 Outline

We begin in Section 2 with some preliminaries and then describe the CWC mode of operation in Section 3. In Section 4 we present our formal statements of security for CWC. Appendix A presents a summary of CWC's properties. Appendix B contains our intellectual property statement. Appendix C contains test vectors.
Versions. In this "submission to NIST" we specify the CWC mode of operation. This is an abbreviated version of $[7]$, which contains security proofs for CWC, further notes on some of the design decisions we made, and more detailed implementation and performance discussions.

## 2 Preliminaries

Notation. If $x$ is a string then $|x|$ denotes its length in bits (not octets). Let $\varepsilon$ denote the empty string. If $x$ and $y$ are two equal-length strings, then $x \oplus y$ denotes the xor of $x$ and $y$. If $X$ and $Y$ are sets, then $\operatorname{Func}(X, Y)$ denotes the set of all functions from $X$ to $Y$ and $\operatorname{Perm}(X)$ denotes the set of all permutations on $X$. If $l$ and $L$ are positive integers, then Func $(l, L)$ denotes the set of all functions from $\{0,1\}^{l}$ to $\{0,1\}^{L}$ and $\operatorname{Perm}(L)$ denotes the set of all permutations on $\{0,1\}^{L}$.

If $N$ is a non-negative integer and $l$ is an integer such that $0 \leq N<2^{l}$, then $\operatorname{tostr}(N, l)$ denotes the encoding of $N$ as an $l$-bit string in big-endian format. If $x$ is a string, then toint $(x)$ denotes the integer corresponding to string $x$ in big-endian format (the most significant bit is not interpreted as a sign bit). For example, toint $(10000010)=2^{7}+2=130$.

Let $x \leftarrow y$ denote the assignment of $y$ to $x$. If $X$ is a set, let $x \stackrel{\&}{\leftarrow} X$ denote the process of uniformly selecting at random an element from $X$ and assigning it to $x$. If $f$ is a randomized algorithm, let $x \stackrel{\&}{\leftarrow} f(y)$ denote the process of running $f$ with input $y$ and a uniformly selected random tape.

When we refer to the time of an algorithm or experiment, we include the size of the code (in some fixed encoding). There is also an implicit big- $\mathcal{O}$ surrounding all time references.

Authenticated encryption schemes with associated data. We use Rogaway's notion of an authenticated encryption with associated data (AEAD) scheme [11].

[^1]An AEAD scheme $\mathcal{S E}=\left(\mathcal{K}_{e}, \mathcal{E}, \mathcal{D}\right)$ consists of three algorithms and is defined over some key space $\operatorname{KeySp}_{\mathcal{S E}}$, some nonce space $\mathrm{NonceSp}_{\mathcal{S E}}=\{0,1\}^{n}, n$ a positive integer, some associated data (header) space $\operatorname{AdSp}_{\mathcal{S E}} \subseteq\{0,1\}^{*}$, and some payload message space $\mathrm{Msg}^{\sin } \subseteq\{0,1\}^{*}$. We require that membership in $\operatorname{MsgSp}_{\mathcal{S E}}$ and $\mathrm{AdSp}_{\mathcal{S E}}$ can be efficiently tested and that if $M, M^{\prime}$ are two strings such that $M \in \mathrm{MsgSp}_{\mathcal{S E}}$ and $\left|M^{\prime}\right|=|M|$, then $M^{\prime} \in \mathrm{MsgSp}_{\mathcal{S E}}$.

The randomized key generation algorithm $\mathcal{K}_{e}$ returns a key $K \in \mathrm{KeySp}_{\mathcal{S} \mathcal{E}}$; we denote this process as $K \stackrel{\&}{\leftarrow} \mathcal{K}_{e}$. The deterministic encryption algorithm $\mathcal{E}$ takes as input a key $K \in \mathrm{KeySp}_{\mathcal{S E}}$, a nonce $N \in \operatorname{NonceSp}_{\mathcal{S E}}$, a header (or associated data) $A \in \mathrm{AdSp}_{\mathcal{S E}}$, and a payload message $M \in \mathrm{MsgSp}_{\mathcal{S E}}$, and returns a ciphertext $C \in\{0,1\}^{*}$; we denote this process as $C \leftarrow \mathcal{E}_{K}^{N, A}(M)$ or $C \leftarrow \mathcal{E}_{K}(N, A, M)$. The deterministic decryption algorithm $\mathcal{D}$ takes as input a key $K \in \operatorname{KeySp}_{\mathcal{S E}}$, a nonce $N \in \operatorname{NonceSp}_{\mathcal{S E}}$, a header $A \in \operatorname{AdSp}_{\mathcal{S E}}$, and a string $C \in\{0,1\}^{*}$ and outputs a message $M \in \mathrm{MsgSp}_{\mathcal{S E}}$ or the special symbol INVALID on error; we denote this process as $M \leftarrow \mathcal{D}_{K}^{N, A}(C)$. We require that $\mathcal{D}_{K}^{N, A}\left(\mathcal{E}_{K}^{N, A}(M)\right)=M$ for all $K \in \operatorname{KeySp}_{\mathcal{S E}}, N \in \operatorname{NonceSp}_{\mathcal{S E}}, A \in \operatorname{AdSp}_{\mathcal{S E}}$, and $M \in \operatorname{MsgSp}_{\mathcal{S E}}$. Let $l(\cdot)$ denote the length function of $\mathcal{S E}$; i.e., for all keys $K$, nonces $N$, headers $A$, and messages $M,\left|\mathcal{E}_{K}^{N, A}(M)\right|=l(|M|)$.
Privacy. Let $\$(\cdot, \cdot, \cdot)$ be an oracle that, on input $(N, A, M) \in \operatorname{NonceSp}_{\mathcal{S E}} \times \operatorname{AdSp}_{\mathcal{S E}} \times \mathrm{MsgSp}_{\mathcal{S E}}$, returns a random string of length $l(|M|)$. Let $B$ be an adversary with access to an oracle and that returns a bit. Then

$$
\operatorname{Adv}_{\mathcal{S E}}^{\text {priv }}(B)=\operatorname{Pr}\left[K \stackrel{\&}{\leftarrow} \mathcal{K}_{e}: B^{\mathcal{E}_{K}(\cdot, \cdot, \cdot)}=1\right]-\operatorname{Pr}\left[B^{\S(\cdot, \cdot,)}=1\right]
$$

is the IND $\$$-CPA-advantage of $B$ in breaking the privacy of $\mathcal{S E}$ under chosen-plaintext attacks; i.e., $\boldsymbol{A d v}_{\mathcal{S E}}^{\text {priv }}(B)$ is the advantage of $B$ in distinguishing between ciphertexts from $\mathcal{E}_{K}(\cdot, \cdot, \cdot)$ and random strings. An adversary $B$ is nonce-respecting if it never queries its oracle with the same nonce twice. Intuitively, a scheme $\mathcal{S E}$ preserves privacy under chosen plaintext attacks if the IND $\$$-CPA-advantage of all nonce-respecting adversaries using reasonable resources is small.
Integrity/authenticity. Let $F$ be a forging adversary and consider an experiment in which we first pick a random key $K \stackrel{\&}{\leftarrow} \mathcal{K}_{e}$ and then run $F$ with oracle access to $\mathcal{E}_{K}(\cdot, \cdot, \cdot)$. We say that $F$ forges if $F$ returns a pair $(N, A, C)$ such that $\mathcal{D}_{K}^{N, A}(C) \neq$ INVALID but $F$ did not make a query $(N, A, M)$ to $\mathcal{E}_{K}(\cdot, \cdot, \cdot)$ that resulted in a response $C$. Then

$$
\mathbf{A d v}_{\mathcal{S} \mathcal{E}}^{\text {auth }}(F)=\operatorname{Pr}\left[K \stackrel{\&}{\leftarrow} \mathcal{K}_{e}: F^{\mathcal{E}_{K}(\cdot,, \cdot)} \text { forges }\right]
$$

is the auth-advantage of $F$ in breaking the integrity/authenticity of $\mathcal{S E}$. Intuitively, $\mathcal{S E}$ preserves integrity if the AUTH-advantage of all nonce-respecting adversaries using reasonable resources is small.
Pseudorandom functions and permutations. Let $F$ be a family of functions from $D$ to $R$. Let $A$ be an adversary with access to an oracle and that returns a bit. Then

$$
\operatorname{Adv}_{F}^{\operatorname{prf}}(A)=\operatorname{Pr}\left[f \stackrel{\&}{\leftarrow} F: A^{f(\cdot)}=1\right]-\operatorname{Pr}\left[g \stackrel{\&}{\leftarrow} \operatorname{Func}(D, R): A^{g(\cdot)}=1\right]
$$

denotes the PRF-advantage of $A$ in distinguishing a random instance of $F$ from a random function. Intuitively, we say that $F$ is a secure PRF if the PRF-advantages of all adversaries using reasonable resources is small.

Let $F$ be a a family of functions from $D$ to $D$. Let $A$ be an adversary with access to an oracle and that returns a bit. Then

$$
\operatorname{Adv}_{F}^{\operatorname{prp}}(A)=\operatorname{Pr}\left[f \stackrel{\&}{\leftarrow} F: A^{f(\cdot)}=1\right]-\operatorname{Pr}\left[g \stackrel{\S}{\leftarrow} \operatorname{Perm}(D): A^{g(\cdot)}=1\right]
$$

denotes the PRP-advantage of $A$ in distinguishing a random instance of $F$ from a random permutation. Intuitively, we say that $F$ is a secure PRP if the PRP-advantages of all adversaries using reasonable resources is small.

We often model block ciphers as pseudorandom functions or permutations. In this case, given a block cipher $E:\{0,1\}^{k} \times\{0,1\}^{L} \rightarrow\{0,1\}^{L}$, we use $E_{K}(\cdot), K \in\{0,1\}^{k}$, to denote the function $E(K, \cdot)$ and we use $f \stackrel{\&}{\leftarrow} E$ as short hand for $K \stackrel{\&}{\leftarrow}\{0,1\}^{k} ; f \leftarrow E_{K}$. We call $k$ the key length of $E$ and we call $L$ the block length.

## 3 The CWC mode of operation

We now describe the CWC mode of operation for 128-bit block ciphers. (See [7] for a description of the general CWC construction.)

If BC denotes a block cipher with 128 -bit blocks and kl-bit keys, and if tl is the desired tag length for CWC in bits, then let CWC-BC-tl denote the CWC mode of operation instantiated with $B C$ using tag length $t l$. Throughout the remainder of this section, fix $B C$ and $t l$ and let CWC-BC-tl $=$ (K, CWC-ENC, CWC-DEC).

We associate to CWC-BC-tl the following sets:

$$
\begin{aligned}
& \text { MsgSp }_{\text {cWc-BC-tl }}=\left\{x \in\left(\{0,1\}^{8}\right)^{*}:|x| \leq \text { MaxMsgLen }\right\} \\
& \text { AdSp }_{\text {cWC-BC-tl }}=\left\{x \in\left(\{0,1\}^{8}\right)^{*}:|x| \leq \text { MaxAdLen }\right\} \\
& \text { KeySp }
\end{aligned}
$$

where MaxMsgLen and MaxAdLen are both $128 \cdot\left(2^{32}-1\right)$. That is, the payload and associated data spaces for CWC-BC-tI consist of all strings of octets that are at most $2^{32}-1$ blocks long.

### 3.1 The CWC core

The key generation algorithm $\mathcal{K}$ returns a randomly selected key from $\mathrm{KeySp}_{\mathrm{cwc}} \mathrm{BC}_{\mathrm{tt}}$ (i.e., the key generation returns a random kl -bit string).

The encryption algorithm CWC-ENC works as follows:

```
Algorithm CWC-ENC \(_{K}(N, A, M)\) // CWC encryption
    \(\sigma \leftarrow \mathrm{CWC}_{-\mathrm{CTR}_{K}(N, M)}\)
    \(\tau \leftarrow \mathrm{CWC}_{-\mathrm{MAC}_{K}}(N, A, \sigma)\)
    Return \(\sigma \| \tau\)
```

where CWC-CTR and CWC-MAC are described in Section 3.2. The decryption algorithm CWC-DEC works as follows:

```
Algorithm \(\mathrm{CWC}_{-\mathrm{DEC}_{K}(N, A, C)} / / \mathrm{CWC}\) decryption
    If \(|C|<\mathrm{tl}\) then return INVALID
    Parse \(C\) as \(\sigma \| \tau\) where \(|\tau|=\mathrm{tl}\)
    If \(A \notin \mathrm{AdSp}_{\mathrm{CWC}-\mathrm{BC}-\mathrm{tl}}\) or \(\sigma \notin \mathrm{MsgSp}_{\mathrm{cwc}-\mathrm{BC}-\mathrm{t}}\) then return INVALID
    If \(\tau \neq \mathrm{CWC}_{-\mathrm{MAC}_{K}}(N, A, \sigma)\) then return INVALID
    Return \(\mathrm{CWC}_{-\mathrm{CTR}_{K}}(N, \sigma)\)
```


### 3.2 The CWC subroutines

The remaining CWC algorithms are defined as follows:

```
Algorithm CWC-CTR \(_{K}(N, M)\) // CWC counter mode module
    \(\alpha \leftarrow\lceil|M| / 128\rceil\)
    For \(i=1\) to \(\alpha\) do
        \(\mathrm{ks}_{i} \leftarrow \mathrm{BC}_{K}\left(10^{7}\|N\| \operatorname{tostr}(i, 32)\right) \quad / /\) Note that \(10^{7}\) means a one bit followed by 7 zeros
    \(\sigma \leftarrow\left(\right.\) first \(|M|\) bits of \(\left.\mathrm{ks}_{1}\left\|\mathrm{ks}_{2}\right\| \cdots \| \mathrm{ks}_{\alpha}\right) \oplus M\)
    Return \(\sigma\)
```

Algorithm $\mathrm{CWC}_{-\mathrm{MAC}_{K}}(N, A, \sigma)$ // CWC authentication module
$R \leftarrow \mathrm{BC}_{K}\left(\mathrm{CWC}-\mathrm{HASH}_{K}(A, \sigma)\right)$
$\tau \leftarrow \mathrm{BC}_{K}\left(10^{7}\|N\| 0^{32}\right) \oplus R$
Return first $t l$ bits of $\tau$
Algorithm CWC-HASH ${ }_{K}(A, \sigma)$ // CWC universal hashing module
$Z \leftarrow$ last 127 bits of $\mathrm{BC}_{K}\left(110^{126}\right)$
$K_{h} \leftarrow \operatorname{toint}(Z) / /$ The same $K_{h}$ value is used in every invocation of CWC-HASH ${ }_{K}$.
$l \leftarrow$ minimum integer such that 96 divides $A \| 0^{l}$
$l^{\prime} \leftarrow$ minimum integer such that 96 divides $\sigma \| 0^{l^{\prime}}$
$X \leftarrow A\left\|0^{l}\right\| \sigma \| 0^{l^{\prime}} ; \beta \leftarrow|X| / 96 ; l_{\sigma} \leftarrow|\sigma| / 8 ; l_{A} \leftarrow|A| / 8$
Break $X$ into chunks $X_{1}, X_{2}, \ldots, X_{\beta} \quad / /\left|X_{1}\right|=\left|X_{2}\right|=\cdots=\left|X_{\beta}\right|=96$
For $i=1$ to $\beta$ do
$Y_{i} \leftarrow \operatorname{toint}\left(X_{i}\right)$
$Y_{\beta+1} \leftarrow 2^{64} \cdot l_{A}+l_{\sigma} \quad / /$ Include the lengths of $A$ and $\sigma$ in the polynomial.
$R \leftarrow Y_{1} K_{h}^{\beta}+\cdots+Y_{\beta} K_{h}+Y_{\beta+1} \bmod 2^{127}-1$
Return $\operatorname{tostr}(R, 128)$ // Note: first bit of result will always be 0

## 4 Theorem statements

The CWC mode is a provably secure AEAD scheme assuming that the underlying block cipher (e.g., AES) is a secure pseudorandom function or pseudorandom permutation. This is a quite reasonable assumption since most modern block ciphers (including AES) are believed to be pseudorandom. Furthermore, all provably-secure block cipher modes of operation that we are aware of make the same assumptions we make (and some modes, e.g. OCB [12], make even stronger, albeit still reasonable, assumptions).

The specific results for CWC appear as Theorem 4.1 and Theorem 4.2 below. The proofs of these theorems appear in [7].

### 4.1 Integrity/authenticity

Theorem 4.1 [Integrity/authenticity of CWC.] Let CWC-BC-tl be as in Section 3. (Recall that the block cipher is BC and tag length is tl.) Consider a nonce-respecting aUth adversary $A$ against CWC-BC-tl. Assume the execution environment allows $A$ to query its oracle with associated data that are at most $n \leq$ MaxAdLen bits long and with messages that are at most $m \leq$ MaxMsgLen bits long. Assume $A$ makes at most $q-1$ oracle queries and the total length of all the payload data
(both in these $q-1$ oracle queries and the forgery attempt) is at most $\mu$. Then given $A$ we can construct a PRF adversary $B_{A}$ and a PRP adversary $C_{A}$ against BC such that

$$
\operatorname{Adv}_{\mathrm{CWWC}}^{\mathrm{auth}-\mathrm{tl}}(A) \leq \mathbf{A d v}_{\mathrm{BC}}^{\mathrm{prf}}\left(B_{A}\right)+\frac{n+m}{2^{133}}+\frac{1}{2^{125}}+\frac{1}{2^{\mathrm{tl}}}
$$

and

$$
\begin{equation*}
\mathbf{A d v}_{\mathrm{CWC}-\mathrm{BC}-\mathrm{tl}}^{\mathrm{auth}}(A) \leq \mathbf{A d v}_{\mathrm{BC}}^{\mathrm{prp}}\left(C_{A}\right)+\frac{(\mu / 128+3 q+1)^{2}}{2^{129}}+\frac{n+m}{2^{133}}+\frac{1}{2^{125}}+\frac{1}{2^{\mathrm{tl}}} \tag{1}
\end{equation*}
$$

Furthermore, the experiments for $B_{A}$ and $C_{A}$ take the same time as the experiment for $A$ and $B_{A}$ and $C_{A}$ make at most $\mu / 128+3 q+1$ oracle queries.

The above theorem means that if the underlying block cipher is a secure pseudorandom function or a secure pseudorandom permutation, then CWC-BC will preserve authenticity. If the underlying block cipher is something like AES, then this initial assumption seems quite reasonable and, therefore, CWC-AES will preserve authenticity.

Let us elaborate on this reasoning. Assume $B C$ is a secure block cipher. This means that $\boldsymbol{A d v} \mathbf{v}_{\mathrm{BC}}^{\mathrm{prp}}(C)$ must be small for all adversaries $C$ using reasonable reasonable resources and, in particular, this means that, for $C_{A}$ as described in the above theorem statement, $\mathbf{A d v}_{\mathrm{BC}}^{\mathrm{prp}}\left(C_{A}\right)$ must be small assuming that $A$ uses reasonable resources. And if $\mathbf{A d v} v_{\mathrm{BC}}^{\mathrm{prp}}\left(C_{A}\right)$ is small and $\mu, q, m$ and $n$ are small, then, because of the above equations, $\operatorname{Adv}_{\mathrm{CWC}-\mathrm{BC}-\mathrm{tI}}^{\text {auth }}(A)$ must also be small as well. I.e., any adversary $A$ using reasonable resources will only be able to break the authenticity of CWC-BC-tl with some small probability.

### 4.2 Privacy

Theorem 4.2 [Privacy of CWC.] Let CWC-BC-tl be as in Section 3. Then given a noncerespecting IND\$-CPA adversary $A$ against CWC-BC-tl one can construct a PRF adversary $B_{A}$ and a PRP adversary $C_{A}$ against BC such that

$$
\mathbf{A d v}_{\mathrm{CWC}-\mathrm{BC}-\mathrm{tl}}^{\text {priv }}(A) \leq \mathbf{A d v}_{\mathrm{BC}}^{\mathrm{prf}}\left(B_{A}\right)
$$

and, if $A$ makes at most $q$ oracle queries totaling at most $\mu$ bits of payload message data,

$$
\begin{equation*}
\mathbf{A d v}_{\mathrm{CWC}-\mathrm{BC}-\mathrm{tl}}^{\mathrm{priv}}(A) \leq \mathbf{A d v}_{\mathrm{BC}}^{\mathrm{prp}}\left(C_{A}\right)+\frac{(\mu / 128+3 q+1)^{2}}{2^{129}} \tag{2}
\end{equation*}
$$

Furthermore, the experiments for $B_{A}$ and $C_{A}$ take the same time as the experiment for $A$ and $B_{A}$ and $C_{A}$ make at most $\mu / 128+3 q+1$ oracle queries.

We interpret Theorem 4.2 in the same way we interpreted Theorem 4.1. In particular, this theorem shows that if BC is a secure pseudorandom function or pseudorandom permutation, then CWC-BC-tl preserves privacy under chosen-plaintext attacks.
Chosen-Ciphertext privacy. Since CWC-BC-tl preserves privacy under chosen-plaintext attacks (Theorem 4.2) and provides integrity (Theorem 4.1) assuming that BC is pseudorandom, it also provides privacy under chosen-ciphertext attacks. (See $[1,11]$ for a discussion of the relationship between chosen-plaintext privacy, integrity, and chosen-ciphertext privacy; this relationship was also used, for example, by [12].)

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## A Summary of properties

In this appendix we summarize some of the properties of CWC. We include all of the properties listed in the submission guidelines on the NIST Modes of Operation website. We also discuss some additional properties that we feel are important.
Security function. CWC is a provably secure authenticated encryption with associated data (AEAD) scheme. Informally, this means that the encapsulation algorithm, on input a pair of messages $(A, M)$ and some nonce $N$, encapsulates $(A, M)$ in a way that protects the privacy of $M$ and the integrity of both $A$ and $M$. Our formal security statements appear in Section 4 and the proofs appear in [7].
Error propagation. Assuming that the underlying block cipher is a secure pseudorandom function or permutation, any attempt, by an adversary using reasonable resources, to forge a new ciphertext will, with very high probably, be detected. This follows from the fact that CWC is a provably-secure AEAD scheme.
Synchronization. Synchronization is based on the nonce. As with other nonce-based AEAD schemes, the nonce must either be sent with the ciphertext or the receiver must know how to derive the nonce on its own.
Parallelizability. CWC is parallelizable. The amount of parallelism for the hashing portion can be determined by the implementor without affecting interoperability.
Keying material required. CWC is defined to be a single-key AEAD scheme. However, CWC does internally use two keys (the main block cipher key and a hash key which is derived using the block cipher key). Implementors can decide whether to store the derived hash key in memory or whether to re-derive it as needed.
Counter/IV/nonce requirements. CWC uses a 11-octet nonce. CWC is provably secure as long as one does not query the encryption algorithm twice with the same nonce. Although it is possible to instantiate the generic CWC paradigm with other nonce lengths, for CWC the nonce size is fixed at 11 -octets in order to minimize interoperability issues.
Memory requirements. The software memory requirements are basically those of the underlying block cipher. For example, fast AES in software requires 4 K bytes of table, and about 200 bytes of expanded key material. In some situations, software implementations may precompute powers of the hash subkey.
Pre-processing capability. The underlying CTR mode keystream can be precomputed. The only block cipher input that cannot be precomputed is the output of CWC-HASH.

CWC can preprocess its associated data, thereby reducing computation time if the associated data remains static or changes only infrequently.
Message length requirements. The associated data and message can both be any string of octets with length at most $128 \cdot\left(2^{32}-1\right)$ bits. Because there does not appear to be a need to handle
strings of arbitrary bit-length, CWC as currently specified cannot encapsulate arbitrary bit-length messages. (As discussed in [7], it is easy to modify CWC to handle arbitrary bit-length messages, if desired.)
Ciphertext expansion. The ciphertext expansion is the minimum possible while still providing a tl-bit tag. That is, on input a pair $(A, M)$, a nonce $N$, and a key $K, \operatorname{CWC}^{-E N C} C_{K}(N, A, M)$ outputs a ciphertext $C$ with length $|C|=|M|+\mathrm{tl}$.
Block cipher invocations. If the hash subkey $K_{h}$ is computed as part of the key generation process and not during each invocation of the CWC encapsulation routine, then CWC makes one block cipher invocation during key setup and $\lceil|M| / 128\rceil+2$ block cipher invocations during encapsulation and decapsulation. If the hash subkey $K_{h}$ is not computed as part of the key generation process, then CWC makes no block cipher invocations during key setup and $\lceil|M| / 128\rceil+3$ block cipher invocations during encapsulation and decapsulation.

Provable security. CWC is a provably-secure AEAD scheme assuming that the underlying block cipher (e.g., AES) is a secure pseudorandom function or permutation. The proofs of security do not require the block cipher to satisfy the strong notion of super-pseudorandomness required by some other block cipher modes of operation.
Number of options and interoperability. CWC uses a minimal number of options. The only options are the choice of the underlying block cipher and the tag length. Having fewer options makes interoperability easier.
On-LINE. The CWC encryption algorithm is on-line. This means that CWC can process data as it arrives, rather than waiting for the entire message to be buffered before beginning the encryption processes. This may be advantageous when encrypting streaming data sources. (Note, however, that, like any other AEAD scheme, the decryptor should still buffer the entire message and check the tag $\tau$ before revealing the plaintext and associated data.)

Patent status. To the best of our knowledge CWC is not covered by any patents.
Performance. CWC is efficient in both hardware and software. In hardware, CWC can process data at 10 Gbps .
Simplicity. Although simplicity is a matter of perspective, we believe that CWC is a very simple construction. It combines standard CTR mode encryption with the evaluation of a polynomial modulo $2^{127}-1$. Because of its simplicity, we believe that CWC is easy to implement and understand.

## B Intellectual property statement

The authors hereby explicitly release any intellectual property rights to CWC mode into the public domain. Further, the authors are not aware of any patent or patent application anywhere in the world that cover this mode.

## C Test vectors

```
Vector #1: CWC-AES-128
AES KEY: 00 01 02 03 04 05 06 07 08 09 OA OB OC OD OE OF
PLAINTEXT: 00 01 02 03 04 05 06 07
ASSOC DATA: <None>
NONCE: FF EE DD CC BB AA 99 88 77 66 55
```

| HASH KEY: | 34 | AE | $6 A$ | $6 F$ | E9 | 51 | 78 | 94 | AC | CC | BB | $9 E$ | BA | E7 | 20 | $8 C$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| HASH VALUE: | $2 B$ | $9 E$ | AE | BE | 67 | $3 F$ | AE | 03 | $6 B$ | 16 | EA | 31 | DC | A7 | AE | $6 B$ |
| AES (HVAL) : | FC | DC | 06 | $4 C$ | CD | CA | FE | E3 | DE | $7 A$ | A3 | CF | $5 C$ | $5 D$ | B9 | $7 B$ |
| MAC CTR PT: | 80 | FF | EE DD | CC | BB | AA | 99 | 88 | 77 | 66 | 55 | 00 | 00 | 00 | 00 |  |
| AES (MCPT) : | AB | 89 | DD | E9 | C4 | 55 | C1 | FE | BE | $7 E$ | E7 | 58 | 82 | D4 | $8 A$ | D2 |
| CIPHERTEXT: | 88 | B8 | DF | 06 | 28 | FD | 51 | CC | 57 | 55 | DB | A5 | 09 | $9 F$ | $3 F$ | $1 D$ |



Vector \#3: CWC-AES-256
AES KEY: 00010203040506070809 OA OB OC OD OE OF
FO EO DO CO BO AO $9080 \quad 70 \quad 60 \quad 5040$
PLAINTEXT: 0001020304050607
ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 77 \quad 66$

| HASH KEY: | 35 | $8 F$ | $2 B$ | OC | FF | E9 | 84 | BE | F9 | EE | EE | 55 | 85 | 36 | BC | E5 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| HASH VALUE: | 18 | 99 | E1 | A6 | 1E | $6 E$ | 37 | 65 | C6 | $3 A$ | 41 | 99 | 56 | $8 C$ | D1 | BF |
| AES (HVAL) : | 1 C | 56 | 65 | OA | 22 | BC | B5 | 94 | AC | F3 | CA | 24 | 46 | 03 | B8 | 5 E |
| MAC CTR PT: | 80 | FF | EE DD | CC | BB | AA | 99 | 88 | 77 | 66 | 55 | 00 | 00 | 00 | 00 |  |
| AES (MCPT) : | 92 | OA | 3B | 46 | 82 | 25 | 16 | F1 | $5 A$ | A3 | $1 B$ | AE | $8 D$ | EB | 72 | A0 |
| CIPHERTEXT: | $7 B$ | CF | 73 | BE | 46 | $9 C$ | 46 | OB | 8E | $5 C$ | $5 E$ | $4 C$ | A0 | 99 | A3 | 65 |

```
Vector #4: CWC-AES-128
AES KEY: 00 01 02 03 04 05 06 07 08 09 OA OB OC OD OE OF
PLAINTEXT: 00 01 02 03 04 05 06 07
ASSOC DATA: 54 68 69 73 20 69 73 20 61 20 70 6C 61 69 6E 74
    65 78 74 20 6% 68 65 61 64 65 72 2E 00
NONCE: FF EE DD CC BB AA 99 88 77 66 55
HASH KEY: 34 AE 6A 6F E9 51 78 94 AC CC BB 9E BA E7 20 8C
HASH VALUE: 2E A9 2A A5 28 B1 1C 08 1C C8 2F 24 9B E4 19 8D
AES(HVAL): EA 54 F8 3D 56 7F 53 05 88 B1 EA 96 36 79 CD AC
```

```
MAC CTR PT: 80 FF EE DD CC BB AA 99 88 77 66 55 00 00 00 00
AES(MCPT): AB 89 DD E9 C4 55 C1 FE BE 7E E7 58 82 D4 8A D2
CIPHERTEXT: }88\mathrm{ B8 DF 06 28 FD 51 CC 41 DD 25 D4 92 2A 92 FB
    36 CF OD CE B4 AD 47 7E
```

Vector \#5: CWC-AES-192
AES KEY: 00010203040506070809 OA OB OC OD OE OF
FO EO DO CO BO AO 9080
PLAINTEXT: 0001020304050607
ASSOC DATA: $5468697320697320 \quad 612070$ 6C $61696 E 74$
$\begin{array}{llllllllllll}65 & 78 & 74 & 20 & 68 & 65 & 61 & 64 & 65 & 72 & 2 \mathrm{E} & 00\end{array}$
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 77 \quad 66$

HASH KEY: 4 F A8 88 AF 068360 OC AB 3575 EF OA E6 01 A5
HASH VALUE: 60 3F FC $24 \quad 7164$ 2E D9 57 E1 B1 EA F2 F8 B0 34
AES (HVAL) : D8 $39862 \mathrm{~A} \quad 33$ 5A 5468 C8 16 DA 47 69 A2 10 EB
MAC CTR PT: 80 FF EE DD CC BB AA 998877665500000000
AES (MCPT): C6 B6 F4 33 F9 1239 4F 6A 8C B9 D3 F2 7B OC 72
CIPHERTEXT: FO DB A9 74123001 BO 1 E 8F 7219 CA 48 6D 27
A2 9A 6394 9B D9 1C 99
Vector \#6: CWC-AES-256
AES KEY: $\quad 00010203040506070809$ OA OB OC OD OE OF
FO EO DO CO BO AO $90 \quad 80 \quad 70 \quad 60 \quad 5040$
PLAINTEXT: 0001020304050607
ASSOC DATA: $54686973 \quad 206973 \quad 20 \quad 6120706 C \quad 61 \quad 696 E 74$
$\begin{array}{llllllllllll}65 & 78 & 74 & 20 & 68 & 65 & 61 & 64 & 65 & 72 & 2 \mathrm{E} & 00\end{array}$
NONCE: FF EE DD CC BB AA $9988 \quad 77 \quad 6655$
HASH KEY: 35 8F 2B OC FF E9 84 BE F9 EE EE 558536 BC E5
HASH VALUE: OA C6 B1 3957 7F 26 DA 941642 E1 6 D 73 EC B5
AES (HVAL) : 4B A5 AD 1E 74 A2 C5 BE AB D0 DA 4D F4 2983 OC
MAC CTR PT: 80 FF EE DD CC BB AA $99 \quad 88 \quad 7766 \quad 5500000000$
AES (MCPT): 92 OA 3B 46822516 F1 5A A3 1B AE 8D EB 72 AO
CIPHERTEXT: 7B CF 73 BE 46 9C 46 OB D9 AF 9658 F6 87 D3 4F
F1 73 C1 E3 79 C2 F1 AC
Vector \#7: CWC-AES-128
AES KEY: 00010203040506070809 OA OB OC OD OE OF
PLAINTEXT: 00010203040506070809 OA OB OC OD OE
ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 77 \quad 66$
HASH KEY: $\quad 34$ AE 6A 6F E9 517894 AC CC BB 9E BA E7 20 8C
HASH VALUE: 79007472 E1 C8 3696 ED 7A B1 F9 03 6E 94 8B
AES (HVAL) : 2B OF 2469 B1 2B BE 39 C9 4067 BA F1 25 E2 5B
MAC CTR PT: 80 FF EE DD CC BB AA 998877665500000000
AES (MCPT) : AB 89 DD E9 C4 55 C1 FE BE 7E E7 58 82 D4 8A D2

```
CIPHERTEXT: 88 B8 DF 06 28 FD 51 CC 31 E6 6E 57 OB 0F 77 80
```


Vector \#8: CWC-AES-192
AES KEY: 00010203040506070809 OA OB OC OD OE OF
FO EO DO CO BO AO 9080
PLAINTEXT: 00010203040506070809 OA OB OC OD OE
ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 77 \quad 66$

```
HASH KEY: 4F A8 88 AF 06 83 60 OC AB 35 75 EF OA E6 01 A5
HASH VALUE: 2C 5E 3A A4 37 1C 27 D6 E8 6B 76 DC 3D 93 BC 87
AES(HVAL): 48 6E 9C E5 C3 16 3E A6 9C D4 D7 E2 7C 9D 92 D2
MAC CTR PT: 80 FF EE DD CC BB AA 99 88 77 66 55 00 00 00 00
AES(MCPT): C6 B6 F4 33 F9 12 39 4F 6A 8C B9 D3 F2 7B OC 72
CIPHERTEXT: F0 DB A9 74 12 30 01 B0 E1 42 B7 58 87 C9 00 8E
D8 68 D6 3A 04 07 E9 F6 58 6E 31 8E E6 9E A0
```

Vector \#9: CWC-AES-256
AES KEY: 00010203040506070809 OA OB OC OD OE OF
FO EO DO CO BO AO $9080 \quad 70 \quad 60 \quad 5040$
PLAINTEXT: 00010203040506070809 OA OB OC OD OE
ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 77 \quad 66$
--------------------------------------------------------------------
HASH KEY: $\quad 35$ 8F 2B OC FF E9 84 BE F9 EE EE 558536 BC E5
HASH VALUE: 4A 7029 CC 582552 CB 75 AD C9 60 FF B3 F7 55
AES (HVAL) : 2B 64 OE 02 CE 51 DE 22 B2 0F 2A 8D C4 23 CD C0
MAC CTR PT: 80 FF EE DD CC BB AA $99 \quad 8877665500000000$
AES (MCPT) : 92 OA 3B 46 82 $2516 \mathrm{~F} 1 \quad 5 \mathrm{~A}$ A3 1B AE 8D EB 72 A0
CIPHERTEXT: 7B CF 73 BE 46 9C 46 OB 9B C6 2D DE 26 DD 47 B9
6E 3544 4C 74 C8 D3 E8 AC 312349 C8 BF 60
Vector \#10: CWC-AES-128
AES KEY: 00010203040506070809 OA OB OC OD OE OF
PLAINTEXT: 00010203040506070809 OA OB OC OD OE
ASSOC DATA: $54686973 \quad 20697320 \quad 6120706 C \quad 61 \quad 696 E 74$
$\begin{array}{llllllllllll}65 & 78 & 74 & 20 & 68 & 65 & 61 & 64 & 65 & 72 & 2 \mathrm{E} & 00\end{array}$
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 77 \quad 66$
HASH KEY: 34 AE 6A 6F E9 517894 AC CC BB 9E BA E7 20 8C
HASH VALUE: 51 AE 9D 7E 86 BD E0 B2 AA 18 2C 9187 0A 9C A5
AES (HVAL): DF 4830 BD 1D DC E0 59 B1 C2 OB 29 01 4 F 8010
MAC CTR PT: 80 FF EE DD CC BB AA $99 \quad 88 \quad 7766 \quad 5500000000$
AES (MCPT) : AB 89 DD E9 C4 55 C 1 FE BE 7E E7 58 82 D4 8A D2
CIPHERTEXT: 88 B8 DF 0628 FD 51 CC 31 E6 6E 57 OB OF 7774
C1 ED 54 D9 8921 A7 OF BC EC 7183 9B OA C2

Vector \#11: CWC-AES-192

| AES KEY: | 00 | 01 | 02 | 03 | 04 | 05 | 06 | 07 | 08 | 09 | $O A$ | $O B$ | $O C$ | $O D$ | $O E$ | $O F$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | FO | O | DO | CO | BO | AO | 90 | 80 |  |  |  |  |  |  |  |  |
| PLAINTEXT: | 00 | 01 | 02 | 03 | 04 | 05 | 06 | 07 | 08 | 09 | OA | OB | OC | OD | OE |  |
| ASSOC DATA: | 54 | 68 | 69 | 73 | 20 | 69 | 73 | 20 | 61 | 20 | 70 | $6 C$ | 61 | 69 | $6 E$ | 74 |
|  | 65 | 78 | 74 | 20 | 68 | 65 | 61 | 64 | 65 | 72 | $2 E$ | 00 |  |  |  |  |
| NONCE: | FF | EE | DD | CC | BB | AA | 99 | 88 | 77 | 66 | 55 |  |  |  |  |  |

HASH KEY: 4 F A8 88 AF 068360 OC AB 3575 EF OA E6 01 A 5 HASH VALUE: 5160 E7 81 DC 64 F9 CD 54 BA 0240 A2 E8 EE 99 AES (HVAL): AO 30581322 B6 805364 BO 3E 5241 D2 2D OA MAC CTR PT: 80 FF EE DD CC BB AA 998877665500000000 AES (MCPT): C6 B6 F4 33 F9 1239 4F 6A 8C B9 D3 F2 7B OC 72 CIPHERTEXT: FO DB A9 74123001 BO E1 42 B7 58 87 C9 0066 86 AC 20 DB A4 B9 1C OE 3C 8781 B3 A9 2178

Vector \#12: CWC-AES-256

| AES KEY: | 00 | 01 | 02 | 03 | 04 | 05 | 06 | 07 | 08 | 09 | OA | OB | OC | OD | OE | OF |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | FO | EO | DO | CO | BO | AO | 90 | 80 | 70 | 60 | 50 | 40 | 30 | 20 | 10 | 00 |
| PLAINTEXT: | 00 | 01 | 02 | 03 | 04 | 05 | 06 | 07 | 08 | 09 | OA | OB | OC | OD | OE |  |
| ASSOC DATA: | 54 | 68 | 69 | 73 | 20 | 69 | 73 | 20 | 61 | 20 | 70 | $6 C$ | 61 | 69 | $6 E$ | 74 |
|  | 65 | 78 | 74 | 20 | 68 | 65 | 61 | 64 | 65 | 72 | $2 E$ | 00 |  |  |  |  |
| NONCE: | FF | EE | DD | CC | BB | AA | 99 | 88 | 77 | 66 | 55 |  |  |  |  |  |

HASH KEY: 35 8F 2B OC FF E9 84 BE F9 EE EE 558536 BC E5 HASH VALUE: 3F F5 OC 60 E6 01 7A 3C A1 BB B3 $54 \quad 650285$ 7C AES (HVAL): 3E EF A2 E4 9791828673 OC F6 E9 46 2C CA 15 MAC CTR PT: 80 FF EE DD CC BB AA 998877665500000000 AES(MCPT): 92 OA 3B 46822516 F1 5A A3 1B AE 8D EB 72 AO CIPHERTEXT: 7B CF 73 BE 46 9C 46 OB 9B C6 2D DE 26 DD 47 AC

E5 99 A2 15 B4 947729 AF ED 47 CB C7 B8 B5
Vector \#13: CWC-AES-128
AES KEY: 00010203040506070809 OA OB OC OD OE OF PLAINTEXT: 00010203040506070809 OA OB OC OD OE OF 808182838485868788898 8B 8 8C 8D 8E 8F ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC $\quad$ BB AA $9988 \quad 77 \quad 6655$


Vector \#14: CWC-AES-192
AES KEY: 00010203040506070809 OA OB OC OD OE OF FO EO DO CO BO AO 9080
PLAINTEXT: 00010203040506070809 OA OB OC OD OE OF $80818283 \quad 8485868788898 A 8 B \quad 8 C 8 D 8 E 8 F$
ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC $\quad$ BB AA $9988 \quad 776655$


Vector \#15: CWC-AES-256
AES KEY: 00010203040506070809 OA OB OC OD OE OF FO EO DO CO BO AO $9080 \quad 70605040 \quad 30201000$
PLAINTEXT: 00010203040506070809 OA OB OC OD OE OF 808182838485868788898 8B 8 8C 8D 8E 8F
ASSOC DATA: <None>
NONCE: $\quad$ FF EE DD CC $\quad$ BB AA $9988 \quad 776655$

HASH KEY: 35 8F 2B OC FF E9 84 BE F9 EE EE 558536 BC E5
 AES (HVAL): B7 F6 AE DE A3 9535 FE 039308 DF E0 C7 F1 78 MAC CTR PT: 80 FF EE DD CC BB AA 998877665500000000 AES (MCPT): 92 OA 3B 46822516 F1 5A A3 1B AE 8D EB 72 A0 CIPHERTEXT: 7B CF 73 BE 46 9C 46 OB 9B C6 2D DE 26 DD 47 B5

D2 4106 CA 5D EB 80 A7 B5 71 OA 38 A4 39 8D BA
25 FC $9598 \quad 21$ BO 23 OF 59301371 6D 2C 83 D8

Vector \#16: CWC-AES-128
AES KEY: 00010203040506070809 OA OB OC OD OE OF
PLAINTEXT: 00010203040506070809 OA OB OC OD OE OF

ASSOC DATA: $54686973 \quad 20697320 \quad 61 \quad 2070 \quad 6 \mathrm{C} \quad 61 \quad 69 \quad 6 \mathrm{E} 74$

NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 776655$
HASH KEY: 34 AE 6A 6F E9 517894 AC CC BB 9E BA E7 20 8C HASH VALUE: 05 EE B6 CB DF A6 E5 B8 4C 65 DD F4 8C C8 2523 AES (HVAL): 62 E5 23 FE 48 8F BC 14 E3 7715 6C 4D 0F D0 8B MAC CTR PT: 80 FF EE DD CC BB AA 998877665500000000 AES(MCPT): AB 89 DD E9 C4 55 C1 FE BE 7E E7 58 82 D4 8A D2 CIPHERTEXT: 88 B8 DF 0628 FD 51 CC 31 E6 6E 57 OB OF 77 OF

48 5B 8264 6E CF B9 F9 A0 B0 75 4F D5 9436 5A C9 6C FE 17 8C DA 7D EA 5D 09 F2 34 CF DB 5A 59


```
Vector #18: CWC-AES-256
```

AES KEY: $\quad 00010203040506070809$ OA OB OC OD OE OF
FO EO DO CO BO AO $9080 \quad 70 \quad 60 \quad 5040$
PLAINTEXT: 00010203040506070809 OA OB OC OD OE OF


$\begin{array}{llllllllllll}65 & 78 & 74 & 20 & 68 & 65 & 61 & 64 & 65 & 72 & 2 E & 00\end{array}$
NONCE: $\quad$ FF EE DD CC BB AA $9988 \quad 776655$



[^0]:    ${ }^{1}$ It is always possible to build two totally independent units and process two packets at a time, but this is dramatically more complex, requiring twice the area, plus a load balancer.
    ${ }^{2}$ If desired, it is possible to instantiate the general CWC paradigm with 64 -bit block ciphers, although certain limitations (e.g., nonce size) apply to such variants. We do not present a 64 -bit CWC variant here since we are primarily concerned with new, high-speed systems using AES, not legacy applications.
    ${ }^{3}$ Actually, $Y_{n+1}$ may be more than 96 -bits long, but we ignore that detail here.

[^1]:    ${ }^{4}$ Although we stress that, if desired, it is easy to modify CWC to handle arbitrary bit-length messages. See [7].

