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TERMINAL EQUIPMENTS

GENERAL CHARACTERISTICS OF A 60-CHANNEL TRANSCODER EQUIPMENT

ITU-T Recommendation G.761

(Extract from the *Blue Book*)

NOTES

1 ITU-T Recommendation G.761 was published in Fascicle III.4 of the *Blue Book*. This file is an extract from the *Blue Book*. While the presentation and layout of the text might be slightly different from the *Blue Book* version, the contents of the file are identical to the *Blue Book* version and copyright conditions remain unchanged (see below).

2 In this Recommendation, the expression "Administration" is used for conciseness to indicate both a telecommunication administration and a recognized operating agency.

Recommendation G.761

GENERAL CHARACTERISTICS OF A 60-CHANNEL TRANSCODER EQUIPMENT

(Malaga-Torremolinos, 1984; amended at Melbourne, 1988)

1 General

The 60-channel transcoder implements a conversion between two 30-channel 2048 kbit/s PCM streams and one 60-channel 2048 kbit/s ADPCM stream. In the 30-channel 2048 kbit/s streams, the telephone signals are coded using 64 kbit/s A-law PCM as specified in Recommendation G.711. In the 60-channel 2048 kbit/s stream, the telephone signals are coded using 32 kbit/s ADPCM as specified in Recommendation G.721. Figure 1/G.761 indicates the nomenclature used for the three different signal ports A, B and C.

Note 1 - Administrations should take into account the guidance given in Recommendation G.721 concerning the use and transmission performance of 32 kbit/s ADPCM .

Note 2 - It should be noted that the transcoder equipment described in this Recommendation has a limited capability of transparently transmitting 64 kbit/s data channels and this should be taken into account in the planning of networks which are likely to evolve into an ISDN (see § 3.8).

This Recommendation is divided into two parts:

- § 2 contains the interface requirements associated with the port C. These requirements are not only applicable to the 60-channel transcoder equipment, but could be applied, in the future, to other equipment such as a 60-channel multiplex terminal, a 60-channel terminating unit at a TDM switch, or a TDMA terminal. In these latter cases, the A and B interfaces would be virtual. As well as point-to-point operation, account has been taken of multi-destination operation in TDMA applications.
- § 3 contains the requirements which are specific to a 60-channel transcoder equipment realization.

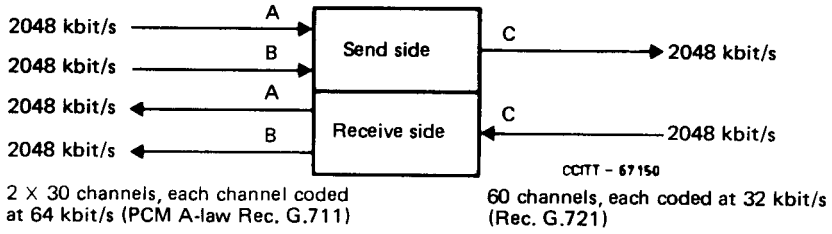

FIGURE 1/G.761

60-channel transcoder ports

2 Characteristics of a 2048 kbit/s signal organized in 64 kbit/s and/or 32 kbit/s time slots (port C)

2.1 *Interface C*

The electrical characteristics of the 2048 kbit/s interface are in accordance with Recommendation G.703, § 6.

2.2 *Frame structure*

The frame structure is in accordance with Recommendation G.704, § 2.3, with bit 1 of time slot 0 used for the cyclic

redundancy check (CRC) procedure.

Time slots 1 to 15 and 17 to 31 each corresponds to:

- either two 4-bit samples of telephone signals coded using 32 kbit/s ADPCM originating from the same incoming PCM stream (A or B); the bit ordering of the 32 kbit/s signals is such that the 4-bit words are transmitted in bit order starting with bit 1 (see §§ 4.2.2 and 4.2.3 of Recommendation G.721). Bits 1 to 4 correspond to the first 32 kbit/s signal and bits 5 to 8 correspond to the second 32 kbit/s signal;
- or a digital signal at 64 kbit/s.

Where stream C is transmitting 60 telephone signals, the numbering of the channels and the correspondence between the 64 kbit/s PCM channels in streams A and B and the 32 kbit/s ADPCM channels in stream C are given in Table 1/G.761.

TABLE 1/G.761

Organization of 2048 kbit/s frame for 60-channels at 32 kbit/s (stream C)

8-bit time slot number	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Bits 1-4 of channel	-	1A	1B	3A	3B	5A	5B	7A	7B	9A	9B	11A	11B	13A	13B	15A
Bits 5-8 of channel	-	2A	2B	4A	4B	6A	6B	8A	8B	10A	10B	12A	12B	14A	14B	16A

8-bits time slot number	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
Bits 1-4 of channel	Note 2	15B	17A	17B	19A	19B	21A	21B	23A	23B	25A	25B	27A	27B	29A	29B
Bits 5-8 of channel	Note 2	16B	18A	18B	20A	20B	22A	22B	24A	24B	26A	26B	28A	28B	30A	30B

Note 1 - The organization of the frame has been chosen to facilitate the direct time slot transfer described in § 3.8.

Note 2 - TS16 is foreseen for signalling but speech channels can be transmitted when necessary. These are numbered 31A and 31B respectively for Bits 1-4 and Bits 5-8 (see § 2.5).

Note 3 - The numbering of the channels from 1 to 30 and the correspondence with the time slots in PCM stream A (respectively B) are given in Recommendation G.735, § 2.

2.3 *Allocation of bits in time slot 0*

The allocation of the bits in time slot 0 is given in Recommendation G.704, § 2.3, with bit 1 of time slot 0 used for the cyclic redundancy check procedure.

Bits 3 to 8 in TS0 of those frames not containing the frame alignment signal are used to transmit:

- alarm indications associated with PCM streams A or B (see §§ 2.6.2 and 2.6.3 dealing with alarm indications);
- spare bits associated with PCM streams A or B (see § 3.3).

2.4 *Frame alignment and CRC procedures*

The strategy for loss and recovery of frame alignment and CRC multiframe alignment is given in Recommendation G.706, § 4.

2.5 *Allocation of bits in TS16*

TS16 can be used:

- either for signalling purposes; namely channel associated signalling (see § 2.5.1) and common channel signalling (see § 2.5.2);
- or, as envisaged in § 5.1 of Recommendation G.704, for the transmission of telephone signals; in this case two samples of telephone signals each coded with 4 bits. Used in this way, stream C can transmit up to 62 telephone signals. Bits 1 to 4 and 5 to 8 of stream C will correspond to 64 kbit/s PCM signals transmitted in TS16 of PCM streams A and B respectively.

2.5.1 *Channel associated signalling*

The allocation of bits in TS16 will depend on the number of signalling bits per channel.

2.5.1.1 *Two or less signalling bits per channel*

This applies to the digital version of Signalling System R2 (see Recommendation Q.421) specified for international applications. This also applies to a number of national signalling systems.

TS16 is organized in multiframes. Each multiframe contains 16 consecutive frames, numbered from 0 to 15. The multiframe repetition frequency is 500 Hz.

2.5.1.1.1 *Allocation of bits in TS16 frame 0*

Table 2/G.761 indicates the content of TS16 frame 0.

Bits 1 to 4 are fixed at 0 and constitute the multiframe alignment signal.

Bits 5 and 8 are used to indicate "AIS in TS16" of PCM streams A and B (see § 2.6.5).

Bits 6 and 7 are used to transmit the remote alarm indications associated with the multiframe of PCM streams A and B (see § 2.6.6).

TABLE 2/G.761

Content of TS16 frame 0

Bit number							
1	2	3	4	5	6	7	8
0	0	0	0	X_5	X_6	X_7	X_8

2.5.1.1.2 *Allocation of bits in TS16 frames 1 to 15*

Table 3/G.761 indicates the content of TS16 in frames 1 to 15.

This allocation of bits provides each 32 kbit/s channel with two signalling channels at 500 bit/s nominated "a" and "b" as defined in Recommendation G.704, § 5.1.3.2.2.

To minimize the risks of simulation of the multiframe alignment signal, special processing of certain signalling bits is carried out as described in § 2.5.1.1.3.

In the case of direct transfer of some 64 kbit/s time slots of PCM streams A or B, the four bits of TS16 associated with the transferred time slots will be transparently transmitted and allocated in accordance with Table 7/G.704. They will not be subject to the special processing described in § 2.5.1.1.3. The four bits of time slot 16 associated with each of the time slots not used in PCM streams A and B because of the direct transfer will be restituted by the transcoder with the following values:

a = 0; b = 1; and d = 1, in conformity with Table 9/G.704.

The signalling distortion of any signalling channel will not be greater than **Error! Bookmark not defined.** 2 ms.

TABLE 3/G.761

Content of TS16 frames 1 to 15

Time slot 16 bit number	1 2	3 4	5 6	7 8
Signalling	Channel a b	Channel a b	Channel a b	Channel a b
Frame 1	1A	2A	15B	16B
Frame 2	1B	2B	17A	18A
Frame 3	3A	4A	17B	18B
Frame 4	3B	4B	19A	20A
Frame 5	5A	6A	19B	20B
Frame 6	5B	6B	21A	22A
Frame 7	7A	8A	21B	22B
Frame 8	7B	8B	23A	24A
Frame 9	9A	10A	23B	24B
Frame 10	9B	10B	25A	26A
Frame 11	11A	12A	25B	26B
Frame 12	11B	12B	27A	28A
Frame 13	13A	14A	27B	28B
Frame 14	13B	14B	29A	30A
Frame 15	15A	16A	29B	30B

Note - The organization of the multiframe ensures consistency with the frame and multiframe organization of Recommendation G.704, § 5.1.3 and allows the possibility of the mixed use in stream C of 32 kbit/s and 64 kbit/s channels with their associated signalling.

2.5.1.1.3 *Special processing of signalling bits*

The signalling bits to be transmitted in bit 2 (respectively 4, 6 and 8) of TS16 (frames 1 to 15) are calculated from B_{n-1}, B_{n-2}, B_{n-3}, b_{n-1} and b_n in accordance with Table 4/G.761 where:

i) b_n is the signalling bit before processing;

ii) B_n is the signalling bit after processing, and

iii) the subscripts n - 3, n - 2, and n - 1 relate to previous signalling bits pertaining to the same telephone channel; more specifically, if b_n is a bit with a given number (2, 4, 6 or 8) in any time slot 16 of frames 1 to 15, then b_{n-1} is the bit with the same number, one multiframe earlier.

Note 1 - It follows from the above that there are 60 individual and independent processing operations at the same time.

The reverse processing (after transmission) is in accordance with Table 5/G.761. The reverse processed value $\hat{b}_n$, is deduced from the successive received bits B_{n-3}, B_{n-2}, B_{n-1}, B_n and from the previous value $\hat{b}_{n-1}$. In the absence of transmission errors in stream C, $\hat{b}_n = b_n$ and there is no increase in signalling distortion. When this is not so, the error multiplication factor lies between 2 and 4.

When, in the case of fault conditions on multiframe A or B (see Table 9/G.761), the signalling bits need to be forced to state 1, this should be implemented on the unprocessed signalling channels (i.e. before the special processing at the send end or after the reverse processing at the receive end). This does not apply to the cases of "partial AIS stream A (B)" considered in § 2.6.2 where the AIS is all 1s, unprocessed.

2.5.1.1.4 *Loss and recovery of multiframe alignment*

The multiframe alignment should be assumed to have been lost when two consecutive multiframe alignment signals have been received in error.

The multiframe alignment should be assumed to have been recovered following detection of an all-zero 4-bit word formed by the first four bits of a time slot 16 and an all-zero 4-bit word one multiframe period later.

2.5.1.2 *More than two signalling bits per channel*

See § 3.8.

2.5.2 *Common channel signalling*

TS16 of stream C can be used for common channel signalling. In this case, its content corresponds, without any modification, to that of TS16 of either PCM stream A or PCM stream B. It should be noted that the simultaneous transfer of TS16 from both streams A and B is not envisaged in this case.

Time slot 16 of PCM stream B (or A) not used because of the direct transfer of time slot 16 of PCM stream A (or B) in time slot 16 of the PCM stream C will be restituted by the transcoder in the form of an all-0s signal or an all-1s signal.

2.6 *Alarm indications*

The following alarm indications can be transmitted in stream C.

2.6.1 *AIS stream C*

This means that a fault, common to the 60 channel has been detected in the send side "AIS stream C" is transmitted as an all ones configuration in stream C.

2.6.2 *AIS stream A (respectively B)*

This means that a fault, common to the 30 channels of stream A (respectively B), has been detected in the send side.

For the send side, the following applies:

When "AIS stream A" and "AIS stream B" are present simultaneously, then "AIS stream C" should be transmitted.

When "AIS stream A" (respectively B) is present, but not "AIS stream B" (respectively A), then the information bits and the signalling bits associated with stream B (respectively A) should be transmitted normally and an all ones configuration should be transmitted in the time slots associated with stream A (respectively B) in stream C and the corresponding bits in TS16. In addition, bit 7 (respectively 8) of TS0 not containing the frame alignment signal in stream C should be set to 1 to indicate the "AIS stream A" (respectively B) (see Table 6/G.761). This configuration in stream C is nominated "partial AIS stream A" (respectively "partial AIS stream B").

For the receive side, the following applies:

Partial AIS stream A (respectively B) will be considered as being present if bit 7 (respectively bit 8) is detected at state 1 on three consecutive occasions.

Partial AIS stream A (respectively B) will be considered as having ceased if bit 7 (respectively 8) is detected at state 0 on three consecutive occasions.

TABLE 4/G.761

Processing of signalling bits (send side)

Input	State				Output
b_n	b_{n-1}	B_{n-3}	B_{n-2}	B_{n-1}	B_n
0	0	0	0	0	1
0	1	0	0	0	0
0	0 or 1	0	0	1	0
0	0 or 1	0	1	0	1
0	0 or 1	0	1	1	0
0	1	1	0	0	0
0	0	1	0	1	1
0	0	1	1	0	1
0	1	1	1	1	0
1	1	0	0	0	1
1	0	0	0	0	0
1	0 or 1	0	0	1	1
1	0 or 1	0	1	0	0
1	0 or 1	0	1	1	1
1	1	1	0	0	1
1	0	1	0	1	0
1	0	1	1	0	0
1	1	1	1	1	1

Note - Other states may be possible, just after powering-on, which may be avoided by careful design.

TABLE 5/G.761

Processing of signalling bits (receive side)

Input	State				Output
B_n	B_{n-3}	B_{n-2}	B_{n-1}	$\hat{b}_{n-1}$	$\hat{b}_n$
0	0	0	0	0	1
0	0	0	0	1	0
1	0	0	0	0	0
1	0	0	0	1	1
0	0	0	1	0 or 1	0
1	0	0	1	0 or 1	1
0	0	1	0	0 or 1	1
1	0	1	0	0 or 1	0
0	0	1	1	0 or 1	0
1	0	1	1	0 or 1	1
0	1	0	0	1	0
1	1	0	0	1	1
0	1	0	1	0	1
1	1	0	1	0	0
0	1	1	0	0	1
1	1	1	0	0	0
0	1	1	1	1	0
1	1	1	1	1	1

Note - Other states may be possible, just after powering-on, which may be avoided by careful design.

TABLE 6/G.761

Use of bits 7 and 8 of TS0 not containing the frame alignment signal in stream C

Bit number	7	8	Meaning
States	1	0	AIS Stream A
	0	1	AIS Stream B
	0	0	Normal
	1	1	Indicates that the safeguarding option (see § 3.10) is being used

2.6.3 *Alarm indication to the remote end for stream A (respectively B)*

In the send side, bit 3 of TS0, not containing the frame alignment signal of stream A (respectively B) should be transferred to bit 3 (respectively 4) of the corresponding TS0 of stream C.

In the receive side, bit 3 (respectively 4) of TS0 not containing the frame alignment signal of stream C should be transferred to bit 3 of the corresponding TS0 of stream A (respectively B).

2.6.4 *AIS in TS16 of stream C*

For channel associated signalling, this means that a fault condition, common to the signalling information associated

with all 60 channels of stream C has been detected in the send side. "AIS in TS16 of stream C" is transmitted as an all ones configuration in TS16.

2.6.5 *AIS in TS16 stream A (respectively B)*

For channel associated signalling, this means that a fault, common to the 30 channels of stream A (respectively B), has been detected in the send side.

For the send side, the following applies:

When "AIS in TS16 stream A" and "AIS in TS16 in stream B" are present simultaneously, then "AIS in TS16 of stream C" should be transmitted. When "AIS in TS16 stream A" (respectively B) is present, but not "AIS in TS16 stream B" (respectively A), then the signalling information of stream B (respectively A) should be transmitted normally, and the signalling bits of TS16 of stream C associated with stream A (respectively B) should be transmitted as an all ones configuration. In addition, bit 5 (respectively 8) of TS16 frame 0 should be set to 1 to indicate "AIS in TS16 stream A" (respectively B).

For the receive side, the following applies:

AIS in TS16 stream A (respectively B) will be considered as being present if bit 5 (respectively 8) of TS16 frame 0 is detected at state 1 on two consecutive occasions.

AIS in TS16 stream A (respectively B) will be considered as having ceased if bit 5 (respectively 8) is detected at state 0 on two consecutive occasions.

2.6.6 *Remote alarm in TS16 of stream A (respectively B)*

For channel associated signalling, this means that a loss of multiframe alignment in stream A (respectively B) has been detected in the opposite direction of transmission.

Bit 6 (respectively 7) of TS16 frame 0 of stream C should be put to 1 to transmit this remote alarm indication associated with stream A (respectively B).

The simultaneous presence of bits 6 and 7 (of TS16 frame 0 of stream C) in state 1 indicates a remote alarm associated with the signalling information of the 60 channels.

3 Other characteristics of the 60 channel transcoder equipment

3.1 *Interfaces A and B*

The electrical characteristics of the two interfaces A and B are in accordance with Recommendation G.703, § 6.

3.2 *Frame structure of streams A and B*

The frame structure of the 2048 kbit/s streams A and B is given in Recommendation G.704, § 2.3, with bit 1 of time slot 0 used for the CRC procedure. The strategy for loss and recovery of frame alignment and CRC multiframe alignment are given in Recommendation G.706, § 4.

As indicated in Recommendation G.704, § 5.1, TS16 of streams A and B can be used for the transmission of telephone signals, if not used for signalling purposes, providing two supplementary channels nominated 31A and 31B respectively. (See Table 1/G.761.)

3.3 *Transparent transfer of bits of time slot 0 not containing the frame alignment signal*

The transcoder equipment should be capable of providing the following two options, the choice between these to be made by the individual Administration or by mutual agreement of the Administrations concerned:

a) bit 4 of time slot 0 not containing the frame alignment signal of streams A and B should be transparently

transmitted in stream C using bits 5 and 6 respectively of time slot 0 not containing the frame alignment signal of stream C;

b) bit 5 should be transmitted in a corresponding way using bits 5 and 6 respectively of stream C.

3.4 *Multiframe structure in TS16 of streams A and B*

When used for channel associated signalling, TS16 of streams A and B is organized in multiframes as defined in Recommendation G.704, § 5.1.3. The definition of the alarm indications and the criteria for multiframe alignment loss and recovery are given in Recommendation G.735.

3.5 *Absolute delay*

The overall absolute delay introduced by a pair of interconnected transcoders (i.e. PCM to PCM) should be less than 500 microseconds for any of the 32 kbit/s channels and for any of the transparently transferred 64 kbit/s channels.

In the case of channel associated signalling, the overall delay introduced by a pair of interconnected transcoders (i.e. PCM to PCM) should be less than 3 milliseconds for any of the signalling channels.

3.6 *Synchronization*

3.6.1 *Send side*

So that the equipment may be inserted into a plesiochronous network, or into a synchronous network operating in degraded conditions, both PCM ports A and B at the send side should be provided with frame and multiframe resynchronizing devices, which initiate controlled sample slips (i.e. sample repetitions or deletions) as required.

It should be possible to synchronize the sending side to any one of the following:

- timing signal associated with incoming PCM stream A;
- timing signal associated with incoming PCM stream B;
- timing signal associated with incoming stream C;
- external 2048 kHz timing signal (see Recommendation G.703, § 10).

In the case of synchronization failure, the consequent action is a prompt maintenance alarm (see Table 8/G.761).

Note - Synchronization failure is assumed in case of a fault condition (see Note 2 to Table 8/G.761) on the incoming signal being used for synchronization.

3.6.2 *Receive side*

The receiving side should be synchronized to the timing signal associated with incoming stream C.

Note - The organization of the network should be such that controlled sample slips are avoided in normal operating conditions. This can lead to the need to synchronize the sending side to the receiving side in remote 30 channel PCM terminals. In circumstances where slips are unavoidable, they will affect both 32 kbit/s channels and directly transferred 64 kbit/s channels.

3.7 *Jitter*

The basic jitter requirements at interfaces are covered by the requirements of §§ 2.1 and 3.1.

When the sending side of the transcoder is synchronized to the incoming PCM stream A or B, and provided both streams A and B are synchronized to each other, slips should not occur when sinusoidal jitter having an amplitude lower then the maximum tolerable input jitter (see Figure 2/G.823) is present at one or both the input ports A and B.

Jitter transfer characteristics between various signal ports are under study.

3.8 *Direct time slot transfer*

It should be possible to manually programme the transcoder to transfer transparently at least two time slots from each of the two incoming PCM streams A and B into stream C.

For national applications, it is sometimes necessary to use more than two signalling bits per channel. In such cases, time slot 16 from PCM streams A and B should be transferred to/from time slots 16 and 17 respectively of stream C. The special processing of signalling bits as described in § 2.5.1.1.3 will not be required.

To be compatible with Recommendation G.735, § 2, at least TS6 and TS22 of each PCM stream A and B should be transferable. The positions of these time slots in the stream C frame are given below:

- TS6 of PCM stream A into TS5 of stream C;
- TS6 of PCM stream B into TS6 of stream C;
- TS22 of PCM stream A into TS22 of stream C;
- TS22 of PCM stream B into TS23 of stream C.

In the case of the transfer of more than two time slots, Table 7/G.761 indicates the allocation up to the maximum possible, taking account of the priority ordering given in Recommendation G.735, § 2.

If (n) 64 kbit/s time slots of PCM stream A (respectively B) are transferred transparently through the transcoder, then the transmission capabilities of PCM stream A (respectively B) will be limited to (30-2n) channels. More precisely, with the frame structure of stream C given in § 2.2:

- when TS6 is transferred transparently, channel 5 of the same PCM stream cannot be used;
- when TS22 is transferred transparently, channel 22 of the same PCM stream cannot be used.

In the case of direct transfer, the transcoder will restitute the binary mark sequence corresponding to amplitude 1 for coding law A (see Recommendation G.712, § 4.3), in the non-usable time slot of PCM output streams A and B.

Note 1 - The need to transmit transparently more than two time slots per incoming PCM stream is under study.

Note 2 - The possibility of remotely controlling the choice of the time slots which are to be transmitted transparently is under study.

Note 3 - The octet sequence integrity is maintained by the transcoder in the case of direct transfer of several time slots at 64 kbit/s.

3.9 *In-service monitoring*

When the PCM to ADPCM and/or the ADPCM to PCM processing functions are multiplexed for 60 channels, in-service monitoring of these processing functions should be provided. This in-service monitoring should be implemented in such a way that it possible to distinguish between failures affecting the send and receive sides separately.

Since no PCM (respectively ADPCM) signals are transmitted in TS0, the in-service monitoring can be implemented by inserting test signals into extra channels corresponding to TS0 of PCM streams A and B.

3.10 *Safeguarding of one PCM stream A or B*

As an option, safeguarding of one PCM tributary can be provided automatically, or otherwise, when a failure of the transcoder digital processing parts, or of the transcoder power supplies, have been detected. In this case the nominated PCM stream, A or B, should, for both directions of transmission, be made to bypass the transcoder and be connected to the transmission link in place of the normal stream C signal.

The simultaneous presence at state 1 of bits 7 and 8 of TS0 not containing the frame alignment signal in stream C on three consecutive occasions is used to indicate to the remote transcoder that the downstream transcoder has been switched to the safeguarding mode. When the safeguarding option is provided automatically, the procedure for the exchange of information between the two transcoders when switching to and from the safeguarding mode is under study.

Note 1 - The choice of the PCM stream (A or B) which will be safeguarded is made when the equipment is installed. It should be the same at both ends of the transmission link.

Note 2 - The use of the safeguarding facility can result in two 2048 kbit/s interfaces in cascade. It is then necessary to ensure, when installing the transcoder, that the combined attenuation of the station cabling at 1024 kHz on both sides of the transcoder is not greater than the maximum attenuation allowed by the equipment connected to the transcoder.

Note 3 - Before using the safeguarding facility, it should be checked that the bits 7 and 8 of TS0 not containing the frame alignment signal of the PCM stream to be safeguarded are in the idle state 1 as specified in Recommendation G.704, § 3.3.1.3.

3.11 *Fault conditions and consequent actions*

3.11.1 *Without safeguarding*

The fault conditions associated with the frames of streams A, B and C and the consequent actions, when the safeguarding option is not used, are given in Table 8/G.761.

When channel associated signalling is used, the fault conditions associated with the multiframes of streams A, B and C and their consequent actions are given in Table 9/G.761.

3.11.2 *With safeguarding*

When the safeguarding option is provided, Tables 8/G.761 and 9/G.761 must be used, with the exception of the fault condition "transcoder failure" of Table 8/G.761. Instead, the fault conditions and consequent actions given in Table 10/G.761 must be used, with reference to Figure 2/G.761.

TABLE 7/G.761

Order of priority for the transfer of 64 kbit/s time slots from streams A or B to stream C

Time slot in stream A	Time slot in stream B	Time slot in stream C
6		5
	6	6
22		22
	22	23
14		13
	14	14
30		30
	30	31
2		1
	2	2
18		18
	18	19
10		9
	10	10
26		26
	26	27
4		3
	4	4
20		20
	20	21
12		11
	12	12
28		28
	28	29
8		7
	8	8
24		24
	24	25
17 (Note 1)		15
	17 (Note 1)	17 (Note 2)

Note 1 - This time slot does not comply with the normal priority ordering given in Recommendation G.735, § 2.

Note 2 - Time slot 17 of stream C will not be available for direct transfer of time slot 17 of stream B when the PCM streams employ time slot 16 channel associated signalling with more than two signalling bits per channel (see § 2.5.1.2).

TABLE 8/G.761

Fault conditions on frames A, B and C and consequent actions without safeguarding

Consequent actions / Fault conditions	Prompt maintenance alarm indication	Bit 3 TSO not containing the FAS stream		TS0 not containing the FAS stream C		Stream C - Apply into bits in TS1-31 associated with stream		AIS applied to stream		AIS applied to stream	TS0 not containing the FAS stream C	
		A	B	bit		A	B	A	B	C	bit 7	bit 8
				3	4	(Note 2)	(Note 3)					
PCM stream A (Note 1)	Yes (Note 4)					11...11					1	
PCM stream B (Note 1)	Yes (Note 4)						11...11					1
PCM streams A and B (Note 1)	Yes (Note 4)									Yes		
Stream C (Note 1)	Yes (Note 4)							Yes	Yes			
TS0 stream A bit 3 to 1 (Note 6)				1								
TS0 stream B bit 3 to 1 (Note 6)					1							
TS0 stream C bit 3 to 1 (Note 6)		1										
TS0 stream C bit 4 to 1 (Note 6)			1									

TABLE 8/G.761 *(cont.)*

Consequent actions / Fault conditions	Prompt maintenance alarm indication	Bit 3 TSO not containing the FAS stream		TS not containing the FAS stream C		Stream C - Apply into bits in TS1-31 associated with stream		AIS applied to stream		AIS applied to stream	TS0 not containing the FAS stream C	
		A	B	bit		A	B	A	B	C	bit 7	bit 8
				3	4	(Note 2)	(Note 3)					
TS0 stream C bit 7 to 1								Yes				
TS0 stream C bit 8 to 1									Yes			
Transcoder failure, send side (Note 5)	Yes									Yes		
Transcoder failure, receive side (Note 5)	Yes							Yes	Yes			
Power supply failure	Yes							Yes, if possible	Yes, if possible	Yes, if possible		
Synchronization failure of the sending side	Yes											

Note 1 - The fault conditions associated with streams A, B and C are: loss of signal, loss of frame alignment, error ratio greater than 10^{-3} as defined in Recommendation G.735, § 4.1.
Note 2 - Only in time slots and signalling bits associated with PCM stream A (both for 32 and 64 kbit/s channels).
Note 3 - Only in time slots and signalling bits associated with PCM stream B (both for 32 and 64 kbit/s channels).
Note 4 - The prompt maintenance alarm indication must be inhibited if the AIS is detected at the corresponding port.
Note 5 - The fault condition "transcoder failure" is detected by the in-service monitoring unit. The transcoder is equipped with such a unit if the digital signal processing is time-multiplexed between the 60-channels.
Note 6 - These fault conditions are not detected by the transcoder. The indications pass transparently through the transcoder (see § 2.6.3).

TABLE 9/G.761

Fault conditions on multiframes A, B and C and consequent actions

Consequent actions / Fault conditions	Prompt maintenance alarm indication	TS 16 frame 0 bit 6		TS 16 frame 0 stream C		TS 16 stream C frames 1-15		AIS TS 16		AIS TS 16	TS 16 frame 0 stream C	
		A	B	6	7	A (Note 2)	B (Note 3	A	B	C	bit 5	bit 8
Multiframe A (Note 1)	Yes (Note 4)					1111					1	
Multiframe B (Note 1)	Yes (Note 4)						1111					1
Multiframe A and B (Note 1)	Yes (Note 4)									Yes		
Multiframe C (Note 1)	Yes (Note 4)							Yes	Yes			
Bit 6 TS 16 frame 0 stream A to 1 (Note 5)				1								
Bit 6 TS 16 frame 0 stream B to 1 (Note 5)					1							
Bit 6 TS 16 frame 0 stream C to 1 (Note 5)		1										
Bit 7 TS 16 frame 0 stream C to 1 (Note 5)			1									
Bits 5 TS 16 frame 0 stream C to 1								Yes				
Bits 8 TS 16 frame 0 stream C to 1									Yes			

Note 1 - The fault condition associated with the three multiframes is the loss of multiframe alignment.
Note 2 - Only in signalling bits associated with PCM stream A. The 1111.... bits are processed in accordance with § 2.5.1.1.3 and Tables 4/G.761 and 5/G.761
Note 3 - Only in signalling bits associated with PCM stream B. The 1111.... bits are processed in accordance with § 2.5.1.1.3 and Tables 4/G.761 and 5/G.761.
Note 4 - The prompt maintenance alarm indication must be inhibited if the AIS in TS16 is detected at the corresponding port.
Note 5 - These fault conditions are not detected by the transcoder. The indications pass transparently through the transcoder (see § 2.6.6).

TABLE 10/G.761

Fault conditions and consequent actions when implementing the safeguarding option

Consequent actions / Fault conditions	Switching to the safeguarding mode		AIS in PCM stream not safeguarded	
	at (1)	at (2)	at (1)	at (2)
Transcoder failure at (1)	Yes		Yes if possible	Yes
Power supply failure at (1)			Yes if possible	Yes
TS0 stream C from (1) to (2) bits 7 and 8 in state 1		Yes		Yes

Note - The transcoder designations (1) and (2) are indicated in Figure 2/G.761.

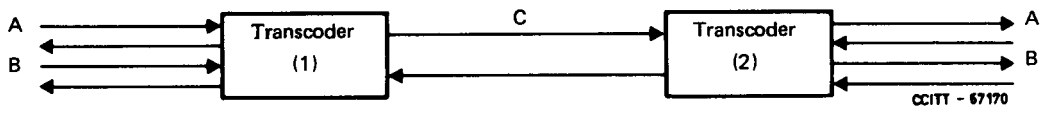

FIGURE 2/G.761

Use of 2 transcoders in a point-to-point link